

Honeywell

**HONEYWELL
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TITLE:

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ENGINEERING PRODUCT SPECIFICATION
Bulk Core Storage Unit
Modular Power System

LEC

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REVISION RECORD

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3.2 REGULATED D-C OUTPUT SEQUENCING AND CONTROL

3.2.1 Low Voltage D-C Sequence (On)

The +5 volt supply for core controller shall be turned on first. The +5 volts to the magnetics module and the -6 volts shall be available after the +5 volts above.

3.2.2 Low Voltage Confidence (Level 1 Confidence)

After all the low voltage supplies are available and within voltage limits a low voltage confidence signal shall be sent to the core controller. Confidence shall be indicated by a logic "0" and lack of confidence will be a logic "1."

3.2.3 High Voltage Enable

After the receipt of low voltage confidence the core controller shall provide a high voltage enable output to cause the high voltage regulators to be turned on. A logic "0" shall allow the regulators to turn on and a logic "1" or an open circuit shall turn them off.

3.2.4 High Voltage Confidence (Level 2 Confidence)

When the high voltage regulators are up to voltage and within voltage limits, a high voltage confidence line shall be sent to the core controller. Confidence is a logic "0" and lack of confidence shall be a logic "1."

3.2.5 Turn Off Sequence

The high voltage regulator outputs, per Section 4, shall conform to Figure 1. This represents the normal (nominal RC time constant) shutdown voltage delay when shutdown is initiated by the loss of phase detection circuit.

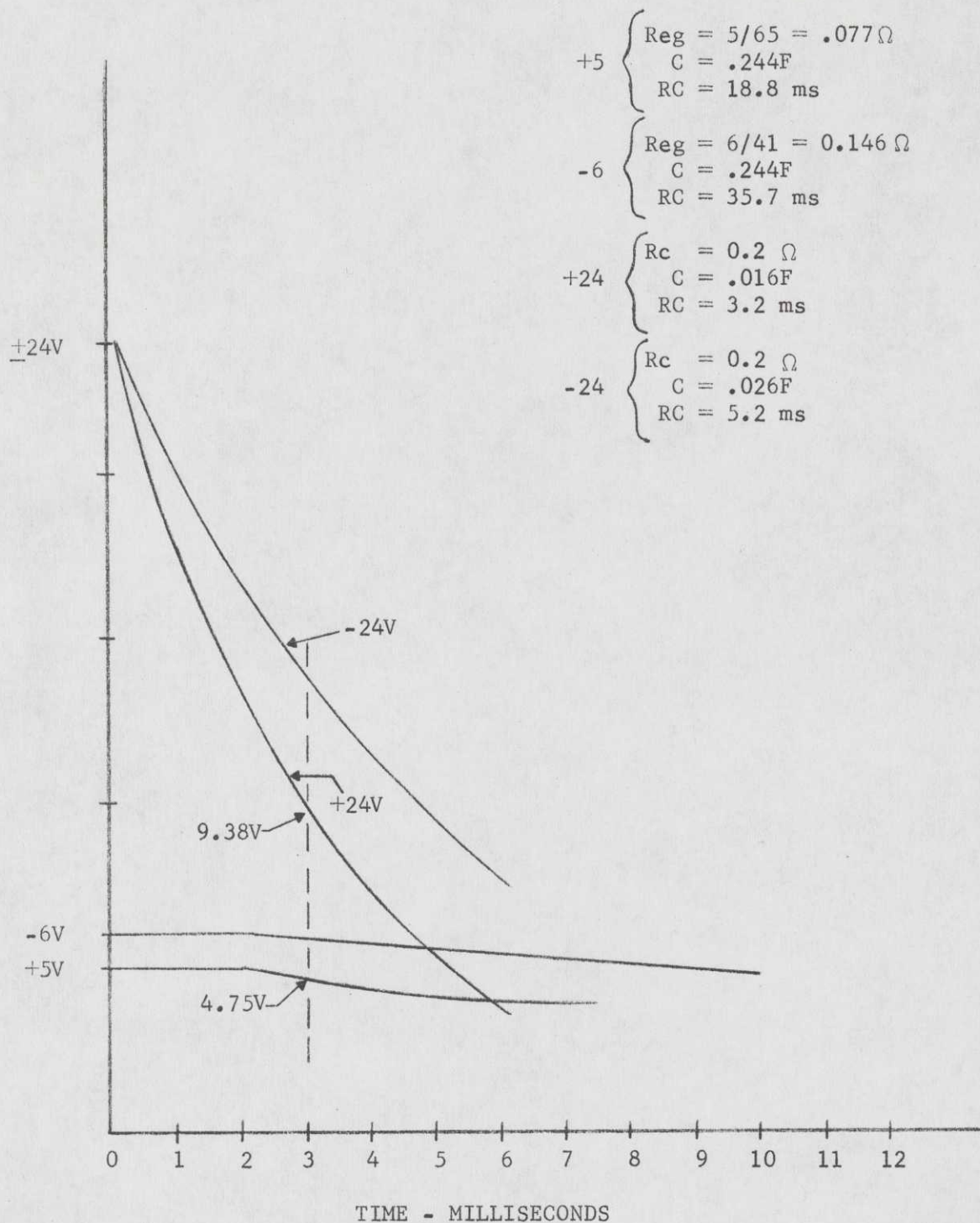
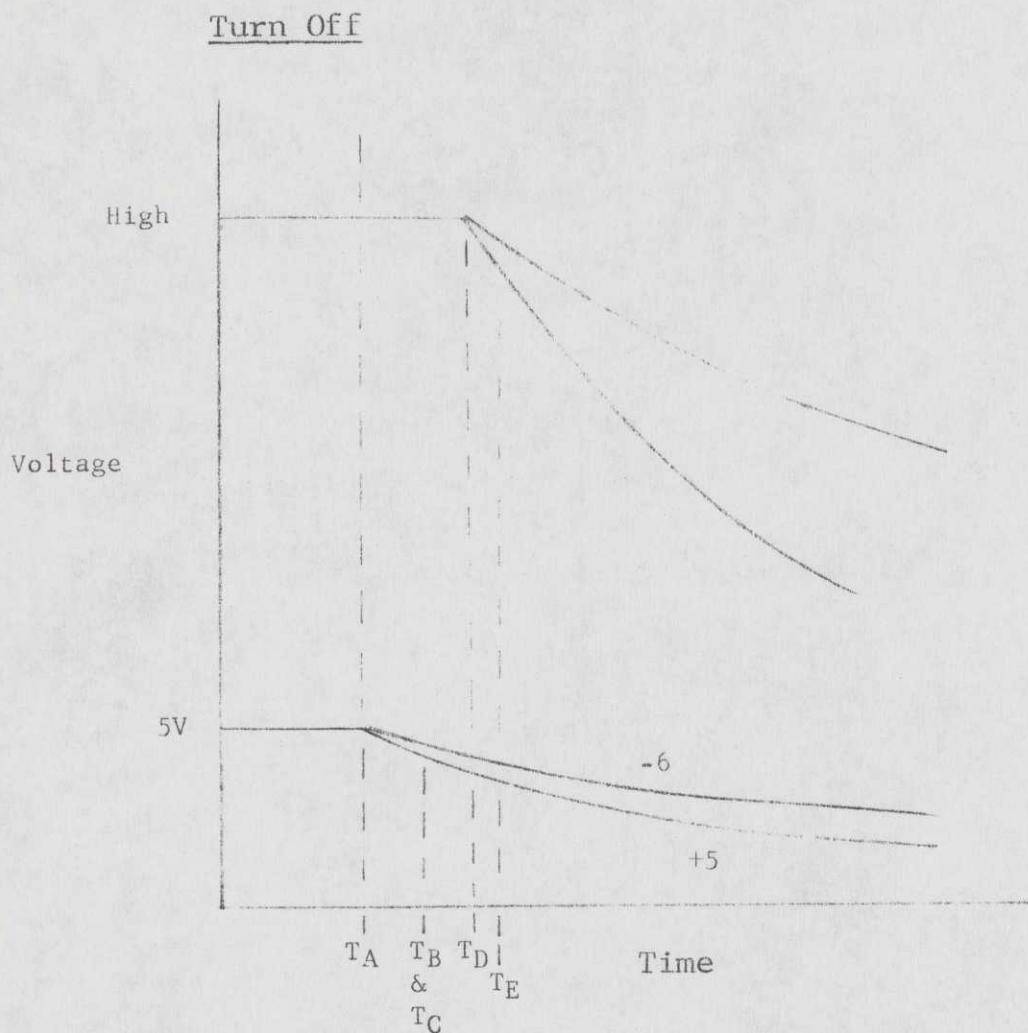


Figure 1



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- T_A = Power Turn Off from a LV Regulator Fault Condition
 T_B = Loss of Level 1 Confidence
 T_C = Loss of High Voltage Enable
 T_D = High Voltage Crowbar Start
 T_E = Loss of Level 2 Confidence